Serial No. 10/790,825 LEE.006 AMENDMENT February 14, 2007

# Remarks/Arguments

## Claim Summary

By this Amendment, claims 7-13 have been cancelled, and new claims 14-20 have been added.

Claims 1-6 and 14-20 are now pending in the application.

## Allowable Claims

Applicants acknowledge with thanks the indicated allowability of original dependent claims 7-13. By this Amendment, claims 7-13 have been cancelled in favor of new claims 14-20, respectively. New claim 14 corresponds to original dependent claim 7 rewritten into independent form. New claims 15-20 correspond to original claims 8-13, respectively.

It is believed that claims 14-20 are in condition for allowance.

#### 35 U.S.C. ¶102

Claims 1-4 were rejected under 35 U.S.C. ¶102 as being anticipated by Park (US 6839048) for the reasons stated at pages 2-3 of the Office Action. Applicants respectfully traverse this rejection.

In the Office Action, the Examiner states that:

"Park teaches ... applying the determined driving current to the backlight module during at least one vertical scanning period (col. 4, lines 22-30)."

Applicants respectfully disagree.

That is, referring to FIG. 1 and col. 4, lines 22-30 of Park, the first adder 212-7 adds the amount of high level R data from the first counter 212-3 during each frame

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and outputs corresponding addition results, and similarly the second adder 212-9 adds the amount of low level R data from the second counter 212-5 during each frame and outputs corresponding addition results. The counter numbers and the amount of high/low level R data are not related to the determined driving current, and the first adder 212-7 and the second adder 212-9 do not output a determined driving current to the backlight module during at least one vertical scanning period as recited in claim. While the single vertical sync signal Vsync triggers the first adder 212-7 and the second adder 212-9 to add the R data, it is not related to the vertical scanning period as defined in claim 1.

Thus, Applicants respectfully contend that claim 1, and claims 2-4 dependent thereon, are not anticipated by Park.

In addition, with respect to claim 4, Park simply discloses that single vertical sync signal Vsync triggers the first adder 212-7 and the second adder 212-9 to add the R data. Park fails to teach that the driving current of the backlight module begins to be modulated every one to sixty vertical scanning periods as recited in claim 4.

### 35 U.S.C. ¶103

Claims 5-6 were rejected under 35 U.S.C. ¶103 as being unpatentable over Park (US 6839048) in view of Sasaki (US 2005/0104838) for the reasons stated at page 3 of the Office Action. Applicants respectfully traverse this rejection for at least the same reasons stated above in connection with the rejection under 35 U.S.C. ¶102.

In addition, Applicants respectfully point out that Sasaki discloses the brightness is the upper predetermined percent of pixel brightness obtained from a histogram of parameters, but does not teach the adjustment of the driving current depending on the percentage. Applicants can find no suggestion or motivation in the references of record to combine and modify the teachings of Park and Sasaki in the fashion suggested by the Examiner.

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# Conclusion

No other issues remaining, reconsideration and favorable action upon the claims 1-6 and 14-20 now pending in the application are requested.

Respectfully submitted, VOLENTINE & WHITT, PLUC

By:

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I HEREBY CERTIFY that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office central facsimile number (571) 273-8300, this 14<sup>th</sup> day of February , 20 07.

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